Academic Period John Landers | Jack Landers - Electrical Computer Engineering Department/Undergraduate (BS) - 09/13/2021 - Inactive - Spring 2025 Quarter

Design Theory, Innovation & Jesuit Principles

Digital Integrated Circuit Design Lab

Introduction to Machine Learning

Digital Integrated Circuit Design

Energy Storage Systems

Course Title

Course Listing 7 items

ECEN 520 - Introduction to Machine Learning

ECEN 153 - Digital Integrated Circuit Design

3.514

ECEN 287 - Energy Storage Systems

Course

Term GPA

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ECEN 196 - Design Project III	Design Project III	1	Α-	3.7	Shoba Krishnan
ANTH 149 - Virtual Santa Clara, History and Culture	Virtual Santa Clara, History and Culture	5	B+	3.3	Lee Panich

Units Grade

B+

Course Enrollment Grade Points Instructor

Long Le

3 Shoba Krishnan

3 Shoba Krishnan

3.3 Ratnesh Sharma

Fatemeh Davoudi Kakhki

MGMT 113 - Design Theory, Innovation & Jesuit Principles

ECEN 153L - Digital Integrated Circuit Design Lab